

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: ICHINOSE et al

Serial No.: 10/733,377

Filed: December 12, 2003

For: Fabrication Method Of Semiconductor  
Integrated Circuit Device

Art Unit:

Examiner:

**PRELIMINARY AMENDMENT**

Mail Stop: Amendment (No Fee)  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 8, 2004

Sir:

The following amendments and remarks are respectfully requested in connection with the above-identified application as listed below and as set forth on the following pages:

A substitute specification along with a marked-up copy of the original specification is attached;

Amendments to the Abstract;

Remarks are included following the amendments